

NBLNA Gain Testing

2286.3 Hz

SR510

	3403C Volts	SR510 Volts	SR510 Corrected Volts	3403C dB	Gain dB	Gain
Input (two 50 ohm to com) single ended input	1	9.76E-06	9.86E-06	9.76E-06	-100.2	
SSM2019 Output	2	1.01E-02	1.02E-02	1.00E-02	-39.9	60.3 1031.6
2nd NonInv IC2 Output pin6	3	8.15E-02	8.27E-02	8.19E-02	-21.7	18.2 8.1
Filter IC3 pin 6 Output	4	7.79E-02	7.84E-02	7.76E-02	-22.2	-0.5 1.0
Inv Gain stage IC4 Output pin 6	5	1.75E-01	1.77E-01	1.75E-01	-15.2	7.0 2.2
NonInv Gain IC 5 pin 6	6	1.44E+00			3.1	18.3 8.2
Transformer output	7	3.26E+00			10.2	7.1 2.3
					Total Gain	110.4 333968